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APPLICANTS

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** CONTINUING DATA ***** *KK*** FOREIGN APPLICATIONS ***** *KIC*

JAPAN P2000-402778 12/28/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED **
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 17 13	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>Kibira Kibira</i> Examiner's Signature Initials				

ADDRESS

22850

TITLE

Simulation method, simulation program, and semiconductor device manufacturing method each employing boundary conditions

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